

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17	(processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR PU OR PE OR SPU) near5 heirarch\$3	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/29 13:51
L2	2	(combin\$3 ADJ2 individual ADJ2 result\$1) SAME (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR PU OR PE)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/29 13:53
S52	847229	(processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR (broadband ADJ engine\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 12:47
S53	271	(process\$3 OR execut\$3 OR evaluat\$3) near5 (data OR program\$1 OR apulet\$1) near5 (secondary ADJ2 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 13:07
S54	11272	(process\$3 OR execut\$3 OR evaluat\$3) near5 (data OR program\$1 OR apulet\$1) near5 ((initial OR main OR first) ADJ2 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:24
S55	78	S53 AND S54	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 13:08
S56	3740	(711/147-148,153-154).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 15:59

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S57	4	S55 AND S56	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 13:08
S59	2	("20020194291").PN.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 13:06
S60	2102	(712/32,34,10-22,25).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 13:06
S61	4	S55 AND S60	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 13:06
S62	6374	(process\$3 OR execut\$3 OR evaluat\$3) near5 (data OR program\$1 OR apulet\$1) near5 ((second\$3 OR additioal OR another) ADJ2 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 13:07
S63	4385	S62 AND S54	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:18
S64	164	S63 AND S56	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 15:59
S65	170	S63 AND S60	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 13:08
S67	7	S64 AND S65	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 13:17
S68	1397	(711/147,153).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 15:59
S69	98	S63 AND S68	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:08

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S70	2150	(711/154).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 15:59
S71	50	S63 AND S70	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 15:59
S72	58	broadband ADJ engine\$1	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:09
S73	8	S72 AND (S64 OR S65)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:47
S75	16	S72 AND (S63) NOT S73	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:14
S77	1	S63 AND (further near5 process\$3)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:17
S78	3427	S62 SAME S54	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S79	141	S78 AND S60	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S80	2916	S62 WITH S54	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S81	122	S80 AND S60	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S82	126	S80 AND S56	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S83	75	S80 AND S68	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:23
S84	10	(processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))) near5 heirarch\$3	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/29 13:45

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S85	3364	(processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))) near5 (delegat\$3 OR instruct\$3) near5 ((another OR secondary OR co OR second) near5 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:43
S86	6	S85 AND S56 AND S60	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 16:44
S87	223	(network ADJ process\$3) AND (S63)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 16:47
S88	23	(network ADJ process\$3) AND (S63) AND (S56 OR S60)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 17:04
S89	2	("20040001445").PN.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2006/04/28 17:04
S90	6425	(compil\$3 OR assembl\$3 OR construct\$3) near5 (result\$1 OR data) near5 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:22
S91	49352	multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 18:29
S92	1134	S90 AND S91	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:23
S93	70	S92 AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:23
S94	1	S92 AND (S56 AND S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:22

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S95	4208	((compil\$3 OR assembl\$3 OR construct\$3) near3 (result\$1 OR data)) near5 (processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:22
S97	744	S95 AND S91	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:23
S98	43	S97 AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 17:23
S10 2	0	(processing near5 data) near5 further near5 ((first OR main) ADJ processor\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 18:13
S10 3	36	(processing near5 data) near5 (further OR more OR addition\$4 OR combin\$3) near5 ((first OR main) ADJ processor\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 18:29
S10 4	8	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((combin\$3) near5 (process\$3 near2 result\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:38
S10 5	1	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((assembl\$3) near5 (process\$3 near2 result\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:03
S10 6	1	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((compil\$3) near5 (process\$3 near2 result\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:06
S10 7	17	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((group\$3) near5 (process\$3 near2 result\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:27
S10 8	0	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1))) near3 (finaliz\$3) near5 (processing))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:29

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S109	0	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) SAME ((processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR PU OR PE) near3 (finaliz\$3) near5 (processing))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:31
S110	2	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) AND ((processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR PU OR PE) near3 (finaliz\$3) near5 (processing))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:31
S111	1060	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) AND ((processor\$1 OR CPU OR microprocessor\$1 OR microcontroller\$1 OR (micro ADJ (controller\$1 OR processor\$1)) OR MPU OR (process\$3 ADJ (element\$1 OR unit\$1)) OR PU OR PE) near3 (finaliz\$3 OR complet\$3 OR finish\$3) near5 (processing))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 20:02
S112	0	L6I AND S56 AND S60	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:33
S113	0	L6I AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:33
S114	94	(multiprocess\$3 OR multi ADJ process\$3 OR co-process\$3 OR coprocess\$3) AND ((combin\$3) near5 (process\$3 near2 result\$1)) NOT S104	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:49
S115	9	S114 AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 19:38
S116	2713	(processing near5 (second\$3 near5 processor\$1)) SAME (processing near5 ((first OR primary OR main) near5 processor\$1))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 20:04
S117	128	S116 AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 20:11

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S11 8	3	S116 AND (S56 AND S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 20:04
S12 1	43	((primary OR main) ADJ2 (processor\$1 OR CPU OR PU OR PE OR MPU OR microprocessor\$1)) near5 (process\$3 OR evaluat\$3) near5 ((result\$1 OR answer\$1) near5 (((secondary OR second OR slave) ADJ2 (processor\$1 OR CPU OR PU OR PE OR MPU OR microprocessor\$1))) OR SPU OR SPE)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/04/28 20:11
S12 2	0	S121 AND (S56 OR S60)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2006/04/28 20:11